



Model Generation in Electronic Design (Current Issues in Electronic Modeling)

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Model Generation in Electronic Design covers a wide range of model applications and research. The book begins by describing a model generator to create component models. It goes on to discuss Asic design and Asic library generation. This section includes chapters on the requirements for developing and Asic library, a case study in which Vital is used to create such a library, and the analysis and description of the accuracy required in modeling interconnections in Asic design.

Other chapters describe the development of thermal models for electronic devices, the development of a set of model packages for Vhdl floating point operations, a techniques for model validation and verification, and a tool for model encryption.

Model Generation in Electronic Design is an essential update for users, vendors, model producers, technical managers, designers and researchers working in electronic design.

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